

# 54/74 FAMILIES OF COMPATIBLE TTL CIRCUITS

## PIN ASSIGNMENTS (TOP VIEWS)

<p><b>QUAD GATES</b></p> <p><b>426</b> 3-STATE OUTPUTS ACTIVE-HIGH ENABLING</p> <p>positive logic: Y = A</p> <p>See page 6-33</p>		<p><del>SN54426 (J, W)</del>    <del>SN74426 (J, N)</del></p>
<p><b>SYSTEM CONTROLLER FOR 8080A</b></p> <p><b>428</b> BIDIRECTIONAL DATA PORTS</p> <p><b>438</b> BIDIRECTIONAL DATA PORTS</p> <p>See page 7-514</p>		<p><del>SN746428 (N)</del> <del>SN746438 (N)</del></p>
<p><b>QUAD TRIDIRECTIONAL BUS TRANSCEIVERS</b></p> <p><b>440</b> OPEN-COLLECTOR NONINVERTED OUTPUTS</p> <p><b>441</b> OPEN-COLLECTOR INVERTED OUTPUTS</p> <p><b>442</b> 3-STATE NONINVERTED OUTPUTS</p> <p><b>443</b> 3-STATE INVERTED OUTPUTS</p> <p><b>444</b> 3-STATE INVERTED AND NONINVERTED OUTPUTS</p> <p><b>448</b> OPEN-COLLECTOR INVERTED AND NONINVERTED OUTPUTS</p> <p>See page 7-507</p>		<p><del>SN54LS440 (J)</del>    <del>SN74LS440 (J, N)</del>  <del>SN54LS441 (J)</del>    <del>SN74LS441 (J, N)</del>  <del>SN54LS442 (J)</del>    <del>SN74LS442 (J, N)</del>  <del>SN54LS443 (J)</del>    <del>SN74LS443 (J, N)</del>  <del>SN54LS444 (J)</del>    <del>SN74LS444 (J, N)</del>  <del>SN54LS448 (J)</del>    <del>SN74LS448 (J, N)</del></p>

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