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MT29F4G08ABAEAWP

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| DATA SHEETS (1) | SPECS | SIM MODELS & SOFTWARE | ROHS CERTIFICATES | DOCUMENTATION & SUPPORT | PARTS WITH THE SAME DATA SHEET (11) | WHERE TO BUY |

Data Sheets (1)

Data Sheet 🔒

4Gb x8, x16 M70M NAND Flash Memory Data Sheet

M70M; Rev. N; Design Revision E

File Type: PDF   **Updated:** 04/2014

Embedded Memory Solutions
NAND Flash
SLC NAND

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Specs

Orderable Parts for: MT29F4G08ABAEAWP

SEE ALL SLC NAND PARTS

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F4G08ABAEAWP:E	Production	N/A	N/A	N/A	N/A	No		N/A

Detailed Specifications

Density	4Gb	**Status**	Production	**RoHS**	Yes
Voltage	3.3V	**Package**	TSOP	**Pin Count**	48-pin
I/O	Common	**Op Temp**	0C to +70C		

Width	x8
MT/s	

Sim Models & Software

Sim Models

Verilog

Verilog: NAND 4Gb SLC M70M **(ZIP)**

4Gb Verilog model Rev 7.30

NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 08/21/2012

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IBIS

IBIS: 4Gb: x8, x16 NAND (Rev E) M70M **(ZIP)**

Rev. 2.0

NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 11/04/2011

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HSpice

HSpice: 4Gb: x8, x16 NAND (Rev E) M70M **(ZIP)**

Rev. 2.0

NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 11/04/2011

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Software

Software Support Pack

YAFFS mark bad block issue **(ZIP)**

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

MLC NAND
NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack

NAND FLASH lockunlock bug fix **(ZIP)**

Reset NAND before write protect check.

MLC NAND
NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 03/31/2016

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Software Support Pack 🔒

NANDcode Flash Translation Layer **(ZIP)**

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

NAND Flash
SLC NAND

File Type: ZIP   **Updated:** 08/08/2014

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MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash
SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver here.

File Type: ZIP   **Updated:** 04/05/2013

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Software Driver 🔒

MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash
Serial NAND
SLC NAND

M68A, M69A, M60A

File Type: ZIP   **Updated:** 12/28/2011

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F4G08ABAEAWP

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)   **Updated:** 12/2016

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F4G08ABAEAWP

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)   **Updated:** 12/2016

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Documentation & Support

See All SLC NAND Documentation

Technical Notes

SEARCH (17) SLC NAND TECHNICAL NOTES

TN-13-49: SLC NAND Device Refresh (PDF)

NAND Flash
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF   **Updated:** 10/31/2016

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TN-29-58: ONFI NV-DDR2 Design Guide (PDF)

NAND Flash
SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF   **Updated:** 09/29/2016

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TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)

NAND Flash
SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF   **Updated:** 08/16/2016

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TN-29-68: One-Time Programmable Operations in 2Gb: x8, x16 NAND Flash Memory (PDF)

NAND Flash
SLC NAND

(TN-29-68) This technical note describes one-time programmable operations in Micron's 2Gb: x8, x16 NAND Flash memory devices.

File Type: PDF   **Updated:** 08/16/2016

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TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)

NAND Flash
SLC NAND

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF   **Updated:** 08/16/2016

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SEARCH (17) SLC NAND TECHNICAL NOTES

Parts with the same Data Sheet (11)

	MT29F4G08ABAEAWP (Current)	MT29F4G16ABAEAH4	MT29F4G08ABAEAH4	MT29F4G08ABAEAH4-IT
Status	Production	Obsolete	Production	Production
Density	4Gb	4Gb	4Gb	4Gb
RoHS	Yes	Yes	Yes	Yes
Width	x8	x16	x8	x8
Voltage	3.3V	3.3V	3.3V	3.3V
Package	TSOP	VFBGA	VFBGA	VFBGA
Pin Count	48-pin	63-ball	63-ball	63-ball
MT/s				
I/O	Common	Common	Common	Common
Op Temp	0C to +70C	0C to +70C	0C to +70C	-40C to +85C

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F4G08ABAEAWP:E	Production	N/A	N/A	N/A	N/A	No		N/A

Contact Your Sales Rep

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